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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Number	10/800983	Docket Number	CNTR.2073
Filed	03/15/04	Group Art Unit	2135
Examiner	KIM Y. VU	Customer No.	23669
Application Title	APPARATUS AND METHOD FOR PROVIDING USER-GENERATED KEY SCHEDULE IN A MICROPROCESSOR CRYPTOGRAPHIC ENGINE		
First Named Inventor	G. GLENN HENRY		

FACSIMILE COVER SHEET

To: Commissioner for Patents - Central Fax Number  
Fax Number: 571-273-8300

From: HUFFMAN PATENT GROUP, LLC  
Fax Number: (661) 460-1986

Pages: 6 (including this cover sheet)

Dear Sir:

Please accept the attached correspondence for the above-identified matter. I hereby certify under 37 CFR 1.8 that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office on the date of signature shown below.

Respectfully submitted,  
HUFFMAN PATENT GROUP, LLC

/Richard K. Huffman/

By: \_\_\_\_\_

RICHARD K. HUFFMAN, P.E.  
Registration No. 41,082  
Tel: (719) 575-9998

07/05/2006

Date: \_\_\_\_\_

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<b>First Named Inventor</b>	G. GLENN HENRY		

## REQUEST FOR CORRECTED FILING RECEIPT

VIA FACSIMILE: 571-273-8300

Mail Stop **AMENDMENT**  
 Commissioner for Patents  
 PO Box 1450  
 Alexandria, VA 22313-1450

Dear Sir:

The Filing Receipt dated 06/01/2004 incorrectly indicates domestic priority data as claimed by applicant. Please add to the domestic priority data shown on the filing receipt, the following domestic priority data:

**This application is a CIP of 10/647057 09/29/2003**  
**Which claims benefit of 60/464394 04/18/2003**

A copy of the Filing Receipt with corrective markings thereon is attached hereto along with a copy of pages 1-2 of the above noted application as filed, which show the claimed domestic priority.

It is requested that the above domestic priority data be added to that claimed for the instant application and that a Corrected Filing Receipt be mailed to the undersigned practitioner.

I hereby certify under 37 CFR 1.8 that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office on the date of signature shown below.

Respectfully submitted,  
**HUFFMAN PATENT GROUP, LLC**

*/Richard K. Huffman/*

By: \_\_\_\_\_

**RICHARD K. HUFFMAN, P.E.**

Registration No. 41,082

Tel: (719) 575-9998

*07/05/2006*

Date: \_\_\_\_\_



**UNITED STATES PATENT AND TRADEMARK OFFICE**

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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/800,983	03/15/2004	2131	878	CNTR.2073	10	26	3

**CONFIRMATION NO. 1410**

23669  
HUFFMAN LAW GROUP, P.C.  
1832 N. CASCADE AVE.  
COLORADO SPRINGS, CO 80907-7449

**FILING RECEIPT**



\*OC000000012827615\*

Date Mailed: 06/01/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing

PAGE 3/6 \* RCVD AT 7/5/2006 12:16:21 PM [Eastern Daylight Time] \* SVR:USPTO-EFAXRF-2/1 \* DNIS:2738300 \* CSID:661-460-1986 \* DURATION (mm-ss):03-129

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**If Required, Foreign Filing License Granted: 05/31/2004**

**Projected Publication Date: 10/21/2004**

**Non-Publication Request: No**

**Early Publication Request: No**

**Title**

Apparatus and method for providing user-generated key schedule in a microprocessor cryptographic engine

**Preliminary Class**

380

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Title 35, United States Code, Section 184  
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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TITLE

APPARATUS AND METHOD FOR  
PROVIDING USER-GENERATED KEY SCHEDULE  
IN A MICROPROCESSOR CRYPTOGRAPHIC ENGINE

by

Thomas A. Crispin

G. Glenn Henry

Terry Parks

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CROSS-REFERENCE TO RELATED APPLICATIONS

[0001] This application claims the benefit of the following U.S. Provisional Applications, which are herein incorporated by reference for all intents and purposes.

<u>SERIAL NUMBER</u>	<u>FILING DATE</u>	<u>TITLE</u>
60/506971	9/29/2003	MICROPROCESSOR APPARATUS AND METHOD FOR OPTIMIZING BLOCK CIPHER CRYPTOGRAPHIC FUNCTIONS
60/507001	9/29/2003	APPARATUS AND METHOD FOR PERFORMING OPERATING SYSTEM TRANSPARENT BLOCK CIPHER CRYPTOGRAPHIC FUNCTIONS
60/506978	9/29/2003	MICROPROCESSOR APPARATUS AND METHOD FOR EMPLOYING CONFIGURABLE BLOCK CIPHER CRYPTOGRAPHIC ALGORITHMS
60/507004	9/29/2003	APPARATUS AND METHOD FOR PROVIDING USER-GENERATED KEY SCHEDULE IN A MICROPROCESSOR CRYPTOGRAPHIC ENGINE

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60/507002	9/29/2003	MICROPROCESSOR APPARATUS AND METHOD FOR PROVIDING CONFIGURABLE CRYPTOGRAPHIC BLOCK CIPHER ROUND RESULTS
60/506991	9/29/2003	MICROPROCESSOR APPARATUS AND METHOD FOR ENABLING CONFIGURABLE DATA BLOCK SIZE IN A CRYPTOGRAPHIC ENGINE
60/507003	9/29/2003	APPARATUS FOR ACCELERATING BLOCK CIPHER CRYPTOGRAPHIC FUNCTIONS IN A MICROPROCESSOR
60/464394	4/18/2003	ADVANCED CRYPTOGRAPHY UNIT
60/506979	9/29/2003	MICROPROCESSOR APPARATUS AND METHOD FOR PROVIDING CONFIGURABLE CRYPTOGRAPHIC KEY SIZE
60/508927	10/3/2003	APPARATUS AND METHOD FOR PERFORMING OPERATING SYSTEM TRANSPARENT CIPHER BLOCK CHAINING MODE CRYPTOGRAPHIC FUNCTIONS
60/508679	10/3/2003	APPARATUS AND METHOD FOR PERFORMING OPERATING SYSTEM TRANSPARENT CIPHER FEEDBACK MODE CRYPTOGRAPHIC FUNCTIONS
60/508076	10/3/2003	APPARATUS AND METHOD FOR PERFORMING OPERATING SYSTEM TRANSPARENT OUTPUT FEEDBACK MODE CRYPTOGRAPHIC FUNCTIONS
60/508604	10/3/2003	APPARATUS AND METHOD FOR GENERATING A CRYPTOGRAPHIC KEY SCHEDULE IN A MICROPROCESSOR

**[0002]** This application is a continuation-in-part of co-pending U.S. Patent Application Serial No. 10/674057 (Docket CNTR.2224) entitled *MICROPROCESSOR APPARATUS AND METHOD FOR PERFORMING BLOCK CIPHER CRYPTOGRAPHIC FUNCTIONS*, which has a common assignee and common inventors, and which was filed on 9/29/2003.